

FIG. 1 is a schematic diagram of a device 10.

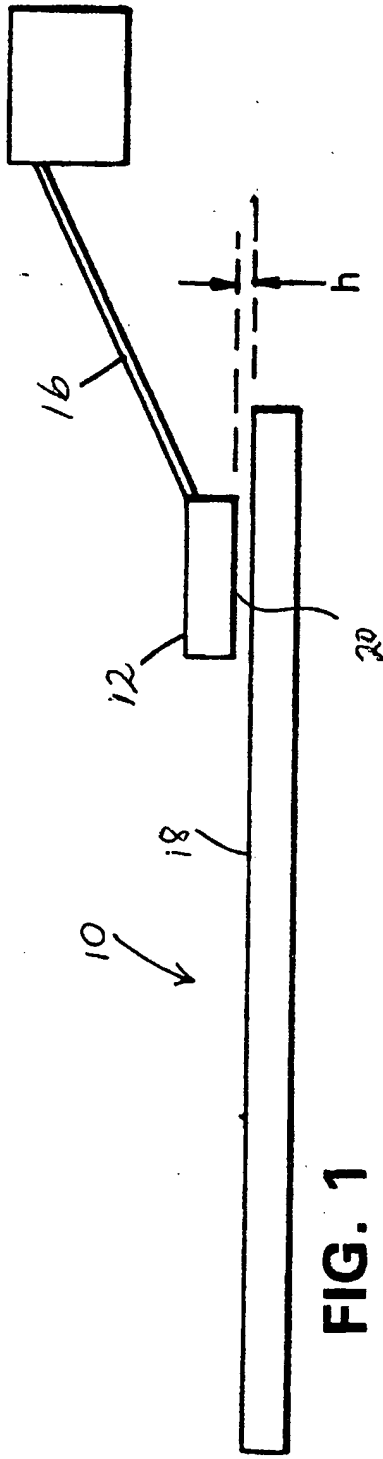


FIG. 1

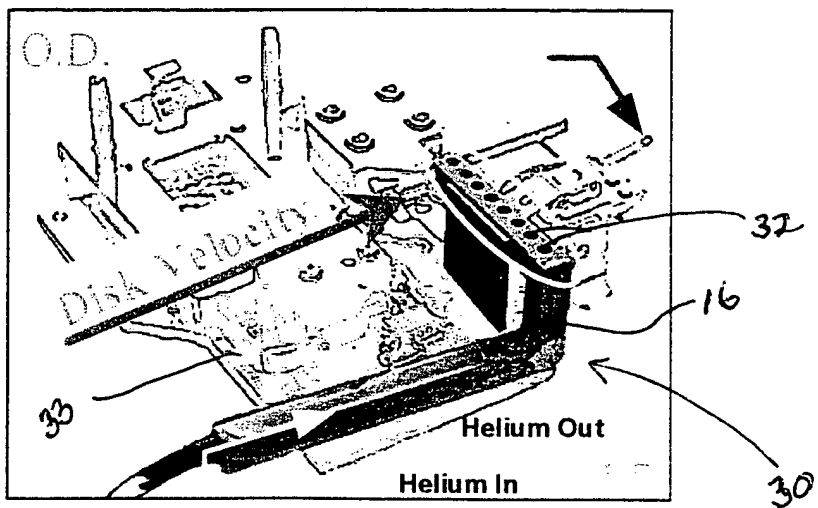


FIG. 1A

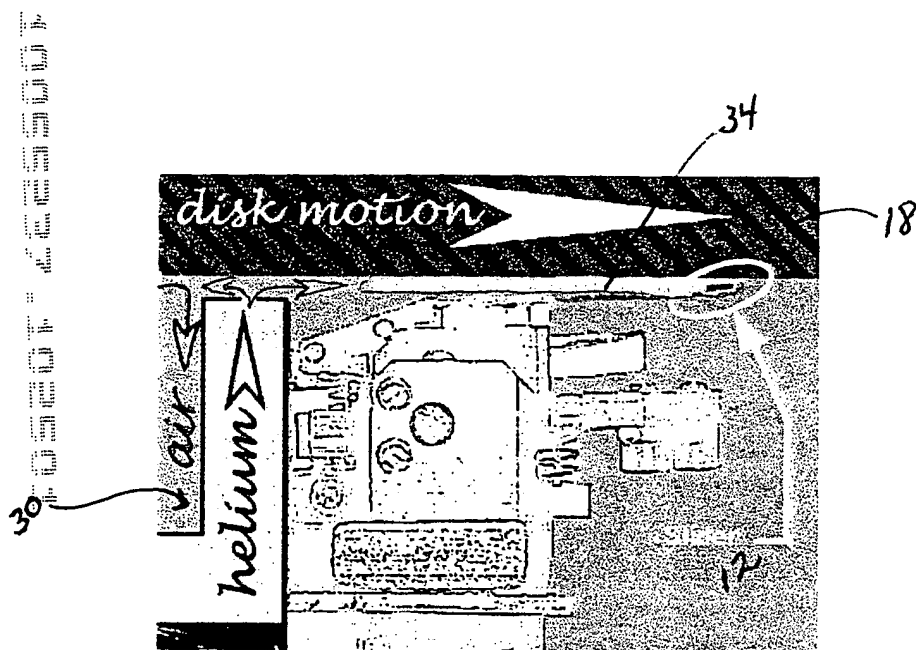
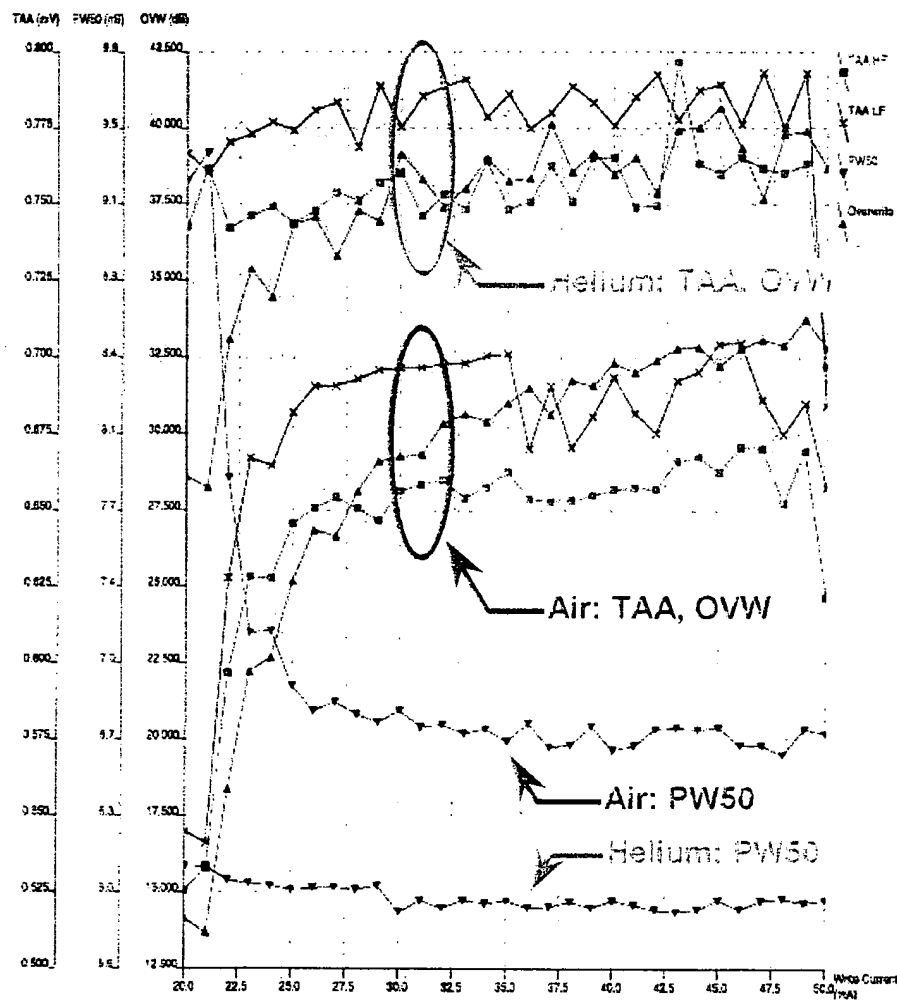
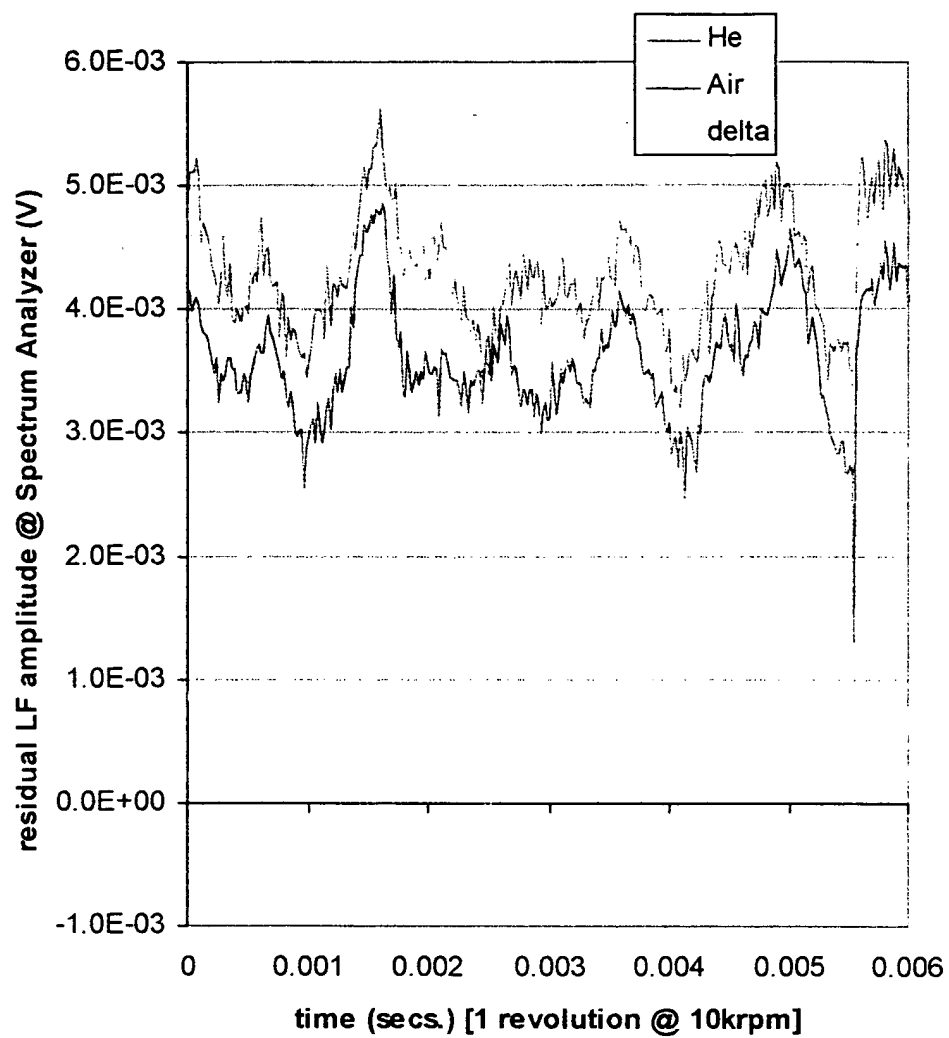


FIG. 2



**FIG. 3**



**FIG. 4**

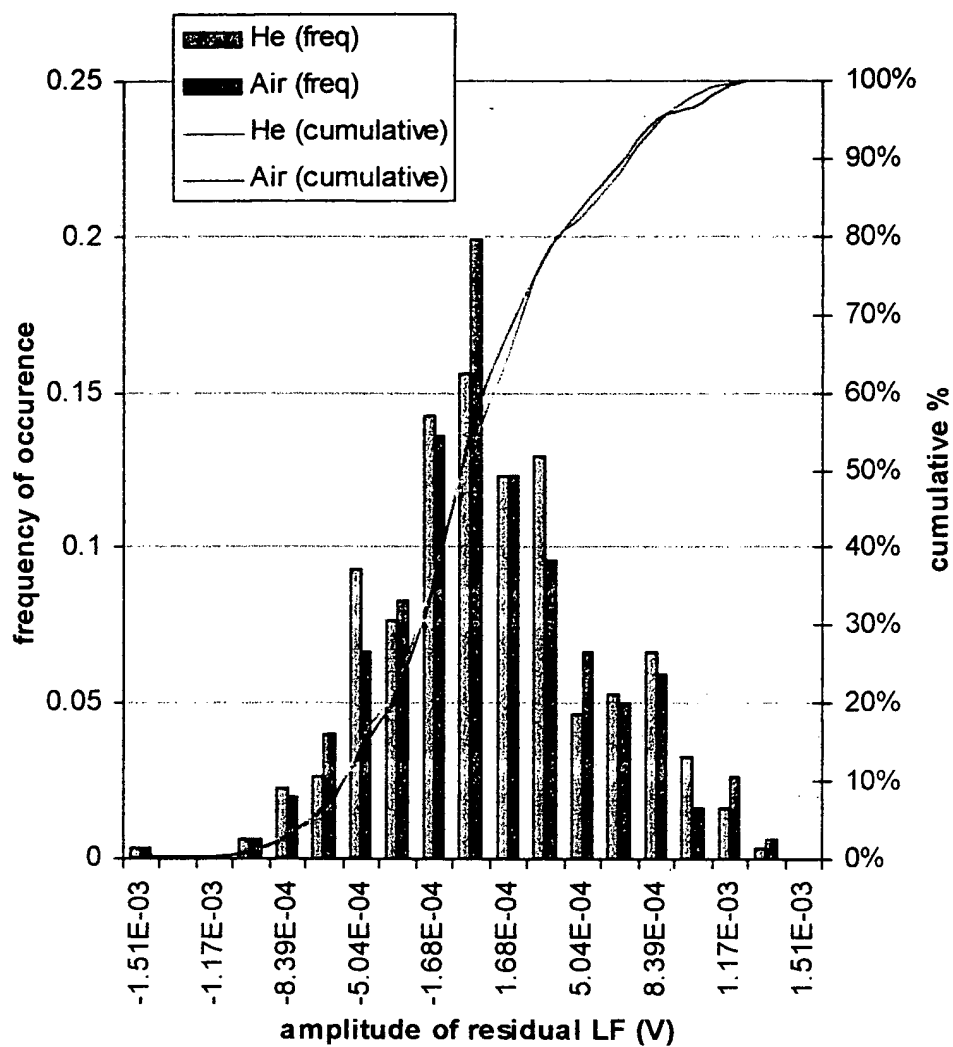
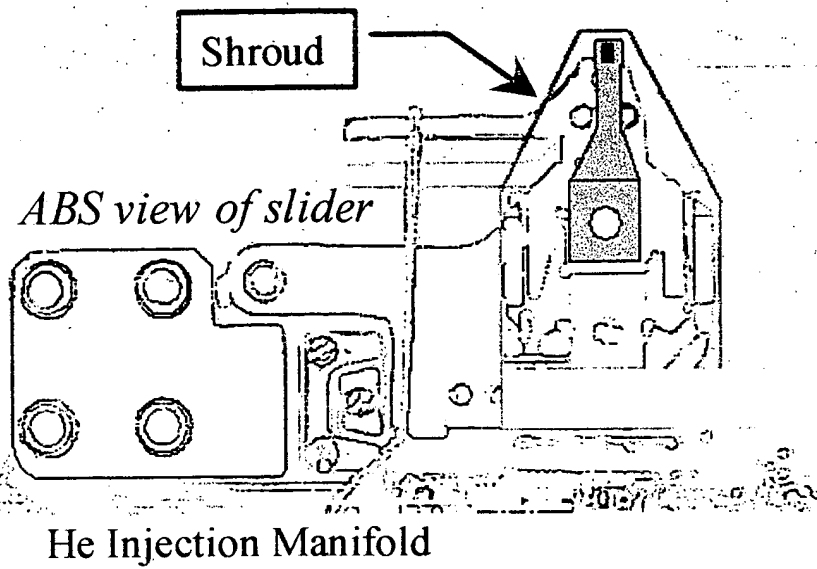


FIG. 5



**FIG. 6**

FIG. 6

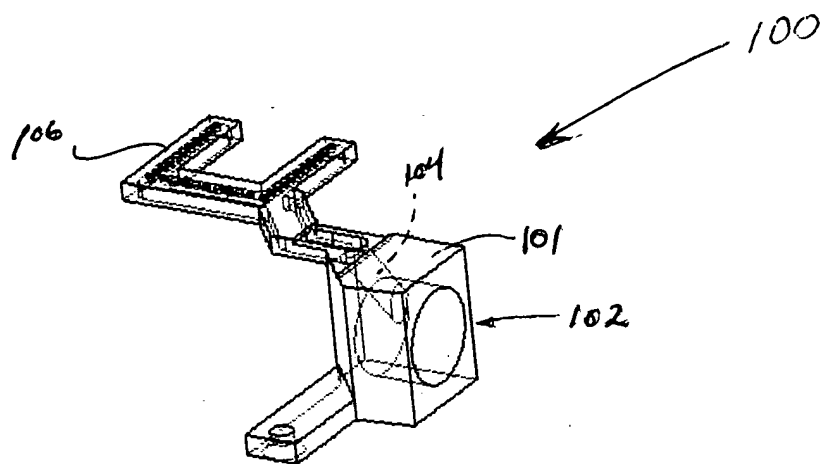


FIG. 7

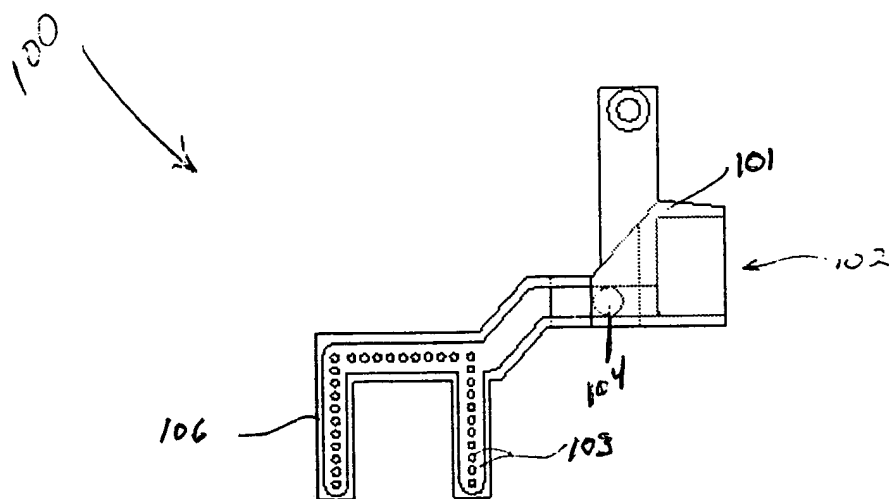


FIG. 7A

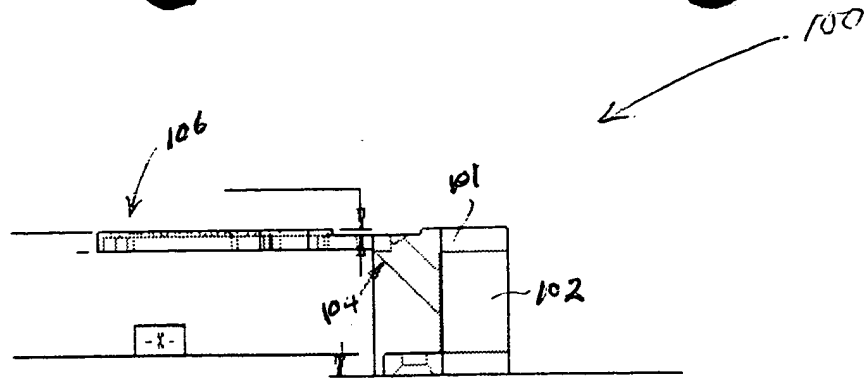


FIG. 7B

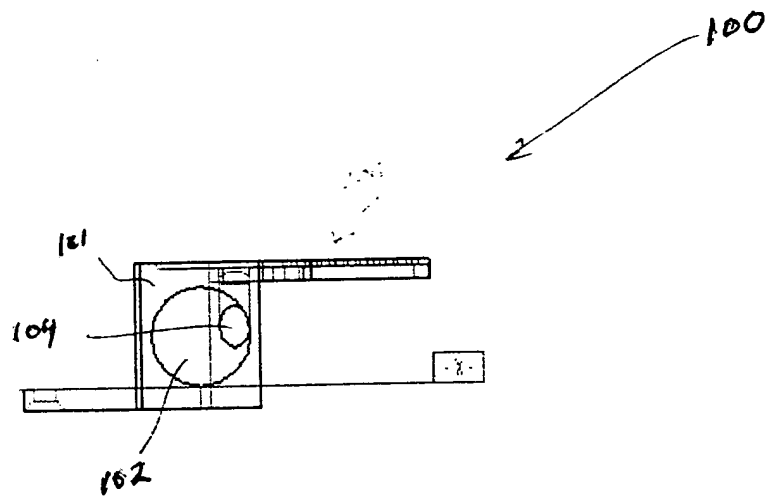


FIG. 7C

FIG. 7B